

Attorney's Docket No. 4363.P005C

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:)
David M. Colleran et al.) Examiner: Not Yet Assigned
Application No.: Not Yet Assigned) Art Unit: Not Yet Assigned
Filed: March 26, 2004)
For: AUTOMATIC PHASE LOCK)
LOOP DESIGN USING)
GEOMETRIC PROGRAMMING)
Which is a continuation of:)
Application No.: 10/119,347) Examiner: Mis, David C.
Filed: April 7, 2002) Art Unit: 2817

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

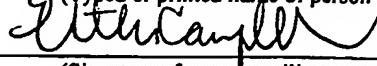
Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on March 26, 2004.

(Date of Deposit)

Esther Campbell

(Typed or printed name of person mailing correspondence)



(Signature of person mailing correspondence)

and IDS Citation Form are being submitted concurrently with the Continuation Application. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s). Some or all of the references listed on the enclosed PTO/SB/08 were previously identified in the parent application (Application No.: 10/119,347, filed April 7, 2002 and copies of the references were furnished at that time.

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

If there are any additional charges, please charge Deposit Account No. 02-2666. A duplicate copy of the Fee Transmittal is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: March 26, 2004


Chui-Kiu Teresa Wong, Reg. No. 48,042

12400 Wilshire Blvd.
Seventh Floor
Los Angeles, CA 90025
(408) 720-8300

Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	TBA
				Filing Date	March 26, 2004
				First Named Inventor	David M. Colleran
				Art Unit	2817
				Examiner Name	Mis, David C.
				Attorney Docket Number	4363P005C
Sheet	1	of	2		

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature	/David Mis/ (09/27/2006)	Date Considered	09/27/2006
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number (optional). *See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 801.04. *Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). *For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. *Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. *Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (08-03) as modified by Blakely, Solotoff, Taylor & Zaifman (wtr) 08/11/2003.

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<p>Substitute for form 1449A/PTO</p> <h1 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h1>				<i>Complete if Known</i>	
Sheet	2	of	2	Application Number	TBA
	Filing Date	March 26, 2004			
	First Named Inventor	David M. Colleran			
	Art Unit	2817			
	Examiner Name	Mis, David C.			
	Attorney Docket Number	4363P005C			

NON PATENT LITERATURE DOCUMENTS

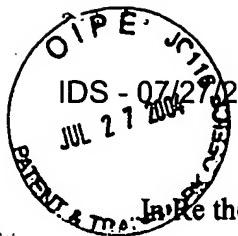
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
DM		CHAN, SHU-PARK; "Analysis of Linear Networks and Systems", Addison-Wesley Publishing Company, 1972, pgs. 23-25, 46 and 47.	

Examiner Signature	/David Mis/ (09/27/2006)	Date Considered	09/27/2006
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**Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.*

*Applicant's unique citation designation number. *Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/088 (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 08/11/2003.
Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450



JFW

Docket No.: 4363P005C

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

DAVID M. COLLERAN, ET AL.

Application No.: 10/810,444

Filed: March 26, 2004

For: **Automatic Phase Lock Loop Design Using
Geometric Programming**

Art Group: TBA

Examiner: TBA

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

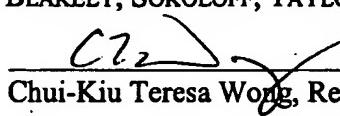
The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: July 23, 2004


Chui-Kiu Teresa Wong, Reg. No. 48,042

12400 Wilshire Boulevard, 7th Floor
Los Angeles, CA 90025
Telephone: (408) 720-8300

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Esther L. Campbell 07-23-04
Date



TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

		Application No.	10/810,444
		Filing Date	March 26, 2004
		First Named Inventor	David M. Colleran
		Art Unit	TBA
		Examiner Name	TBA
Total Number of Pages in This Submission	28	Attorney Docket Number	4363P005C

ENCLOSURES (check all that apply)

<input checked="" type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment / Response <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input checked="" type="checkbox"/> PTO/SB/08 <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/ Incomplete Application <input type="checkbox"/> Basic Filing Fee <input type="checkbox"/> Declaration/POA <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation, Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s)	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below):
		19 cited references and Return Receipt Postcard.
Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Chui-Kiu Teresa Wong, Reg. No. 48,042 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
Signature	
Date	July 23, 2004

CERTIFICATE OF MAILING/TRANSMISSION

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Signature		Date	July 23, 2004

Based on PTO/SB/21 (04-04) as modified by Blakely, Sokoloff, Taylor & Zafman (wz) 08/04/2004.
SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450



FEE TRANSMITTAL for FY 2004

Effective 10/01/2004. Patent fees are subject to annual revision.

Applicant claims small entity status. See 37 CFR 1.27.

TOTAL AMOUNT OF PAYMENT (\$)

0.00

Complete If Known	
Application Number	10/810,444
Filing Date	March 26, 2004
First Named Inventor	David M. Coleran
Examiner Name	TBA
Art Unit	TBA
Attorney Docket No.	4363P005C

METHOD OF PAYMENT (check all that apply)

Check Credit card Money Order Other None
 Deposit Account

Deposit Account Number **02-2666**

Deposit Account Name **Blakely, Sokoloff, Taylor & Zafman LLP**

The Commissioner is authorized to: (check all that apply)

- Charge fee(s) indicated below Credit any overpayments
 Charge any additional fee(s) or underpayment of fees as required under 37 CFR §§ 1.16, 1.17, 1.18 and 1.20.
 Charge fee(s) indicated below, except for the filing fee
 - to the above-identified deposit account

FEE CALCULATION

1. BASIC FILING FEE

Large Entity	Small Entity	Fee Description	Fee Paid
1001	770	2001 365 Utility filing fee	
1002	340	2002 170 Design filing fee	
1003	530	2003 265 Plant filing fee	
1004	770	2004 365 Reissue filing fee	
1005	160	2005 60 Provisional filing fee	
SUBTOTAL (1)		(\$)	

2. EXTRA CLAIM FEES

Total Claims	Extra Claims	Fee from below	Fee Paid
8	20*	0 X 18.00 =	\$0.00
Independent Claims	2	3* = 0 X 88.00 =	\$0.00
Multiple Dependent			

Large Entity	Small Entity	Fee Description
1202	18	2202 9 Claims in excess of 20
1201	88	2201 43 Independent claims in excess of 3
1203	280	2203 145 Multiple Dependent claim, if not paid
1204	88	2204 43 **Reissue independent claims over original patent
1205	18	2205 9 **Reissue claims in excess of 20 and over original patent
SUBTOTAL (2)		(\$) 0.00

*or number previously paid, if greater. For Reissues, see below

3. ADDITIONAL FEES

Large Entity	Small Entity	Fee Description	Fee Paid
1051	130	2051 65 SurchARGE - late filing fee or oath	
1052	50	2052 25 SurchARGE - late provisional filing fee or cover sheet	
2053	130	2053 130 Non-English specification	
1812	2,520	1812 2,520 For filing a request for ex parte reexamination	
1804	620*	1804 620* Requesting publication of SIR prior to Examiner action	
1805	1,840*	1805 1,840* Requesting publication of SIR after Examiner action	
1251	110	2251 65 Extension for reply within first month	
1252	420	2252 210 Extension for reply within second month	
1253	950	2253 475 Extension for reply within third month	
1254	1,480	2254 740 Extension for reply within fourth month	
1255	2,010	2255 1,005 Extension for reply within fifth month	
1404	330	2401 165 Notice of Appeal	
1402	330	2402 165 Filing a brief in support of an appeal	
1403	290	2403 145 Request for oral hearing	
1451	1,510	2451 1,510 Petition to institute a public use proceeding	
1452	110	2452 55 Petition to revive - unavoidable	
1453	1,330	2453 665 Petition to revive - unintentional	
1501	1,330	2501 665 Utility issue fee (or reissue)	
1502	480	2502 240 Design issue fee	
1503	640	2503 320 Plant issue fee	
1460	130	2460 130 Petitions to the Commissioner	
1807	50	1807 50 Prospecting fee under 37 CFR 1.17(q)	
1808	180	1808 180 Submission of Information Disclosure Stmt	
8021	40	8021 40 Recording each patent assignment per property (times number of properties)	
1809	770	1809 385 Filing a submission after final rejection (37 CFR § 1.129(a))	
1810	770	2810 385 For each additional invention to be examined (37 CFR § 1.129(b))	
1801	770	2801 385 Request for Continued Examination (RCE)	
1802	900	1802 900 Request for expedited examination of a design application	
Other fee (specify) _____			

*Reduced by Basic Filing Fee Paid

SUBTOTAL (3)

(\$)

SUBMITTED BY

Name (Print/Type)	Chui-Kiu Teresa Wong	Registration No. (Attorney/Agent)	48,042	Telephone	(408) 720-8300	Complete (If applicable)
Signature	C72				Date	07/23/04

Based on PTO/SB/17 (10-03) as modified by Blakely, Sokoloff, Taylor & Zafman (wtr) 02/10/2004.
 SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

JUL 27 2004

Substitute for Form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Complete if Known	
Application Number	10/810,444
Filing Date	March 26, 2004
First Named Inventor:	Dave Colleran
Art Unit	TBA
Examiner Name	TBA
Attorney Docket Number	4363P005C

Sheet 1 of 3 Attorney Docket Number 4363P005C

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

Examiner Signature	/David Mis/ (09/27/2006)	Date Considered	09/27/2006
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-788-9199) and select option 2.

Substitute for Form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known

Application Number	10/810,444
Filing Date	March 26, 2004
First Named Inventor:	Dave Colleran
Art Unit	TBA
Examiner Name	TBA

Sheet 2 of 3 Attorney Docket Number 4363P005

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
DM		KORTANEK, K.O., et al., "An Infeasible Interior-Point Algorithm For Solving Primal And Dual Geometric Programs," pp., 155-181, Mathematical Programming Society, Inc., 76:155-181, January 1, 1995.	
DM		GIELEN, G., et al., "An Analogue Module Generator For Mixed Analogue/Digital ASIC Design", International Journal of Circuit Theory and Applications, Vol. 23, pp. 269-283, 1995.	
DM		HERSHENSON, M., et al., "Automated Design of Folded-Cascode Op-Amps with Sensitivity Analysis", pp. 121-124, Electronics, Circuits and Systems, IEEE International Conference on LISBOA, September 7-10, 1998.	
DM		HERSHENSON, M., et al., "Optimization of Inductor Circutis via Geometric Programming", pp. 994-998, Design Automation Conference, June 21, 1999, Proceddings.	
DM		MEDIERO, F., et al., "A Vertically Integrated Tool For Automated Design Of Sigma Delta Modulators", IEEE Journal of Solid-State Circuits, Vol. 30., No. 7, July 1, 1995, pp. 762-767.	
DM		HERSHENSON, M., et al., "Optimal Design Of A CMOS Op-Amp Via Geometric Programming", IEEE Transactions On Computer Aided Design Of Integrated Circuits And Systems, Vol. 20., N. 1 January 2001, pp. 1-21.	
DM		MANDAL, P., et al., "CMOS Op-Amp Sizing Using A Geometry Programming Formulation", IEEE Transactions On Computer Aided Design Of Integrated Circuits And Systems, Vol. 20., No. 1, January 31, 2001, pp. 22-38.	
DM		DAEMS, W., et al., "Simulation-based Automatic Generation Of Signomial And Posynomial Performance Models For Analog Integrated Circuit Sizing", IEEE/ACM International Conference On Computer-Aided Design, November 4, 2001, pp. 70-74.	
DM		VON KAENEL, V., et al., "A 320MHz, 1.5mW at 1.36V CMOS PLL For Microprocessor Clock Generation", IEEE Solid-State Circuits Conference, February 9, 1996, Digest of Technical Papers, 42nd ISSCC96/ SESSION 8 / DIGITAL CLOCKS AND LATCHES / PAPER FA 8.2.	
DM		YOUNG, et al., "A PLL Clock Generator with 5 to 110 MHz of Lock Range for Microprocessor", IEEE Journal of Solid-State Circuits, Vol. 27, No. 11, November 1992, pp. 1599-1607.	
DM		NOVOF, et al., "Fully Integrated CMOS Phase-Locked Loop with 15 to 240 MHz Locking Range and + 50 ps Jitter", IEEE Journal of Solid-State Circuits, Vol. 30., No. 11, November 1995, pps. 1259-1266.	

Examiner Signature	/David Mis/ (09/27/2006)	Date Considered	09/27/2006
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English Translation is attached.

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Examiner Signature	/David Mis/ (09/27/2006)	Date Considered	09/27/2006
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Attorneys Docket No. 004363.P005CPATENT
JFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

David M. Colleran

Application No.: 10/810,444

Filed: March 26, 2004

For: Automatic Phase Lock Loop Design
Using Geometric Programming

Examiner: Not Yet Assigned

Art Unit: Not Yet Assigned

Mail Stop Amendment
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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(Date of Deposit)

Tina Wainright

(Typed or printed name of person mailing correspondence)

Tina Wainright

(Signature of person mailing correspondence)

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

- X 37 C.F.R. §1.97(b).
- 37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:
- A statement pursuant to 37 C.F.R. §1.97(e) or
- A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).
- 37 C.F.R. §1.97(d). If so, then enclosed with this Information Disclosure Statement are the following:
- (1) A statement pursuant to 37 C.F.R. §1.97(e); and
 - (2) A check for \$180.00 for the fee under 37 C.F.R. §1.17(p) for submission of the Information Disclosure Statement.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 9/2, 2004


Chui-Kiu Teresa Wong
Reg. No. 48,042

12400 Wilshire Blvd.
Seventh Floor
Los Angeles, CA 90025
(408) 720-8300

Substitute for Form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)



SEP 07 2004

Sheet	1	of	3	Attorney Docket Number	004363.P005C
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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
DM		us-	6,578,179 B2	06-10-2003	Shirotori, et al.	
DM		us-	6,574,786 B1	06-03-2003	Pohlenz, et al.	
DM		us-	6,539,533 B1	03-25-2003	Brown, III et al.	
DM		us-	6,581,188	06-17-2003	Hosomi, et al.	
DM		us-	6,311,315	10-30-2001	Tamaki	
DM		us-	6,002,860	12-14-1999	Voinigescu, et al.	
DM		us-	5,754,826	05-19-1998	Gamal, et al.	
DM		us-	5,633,807	05-27-1997	Fishburn, et al.	
DM		us-	5,055,716	10-08-1991	El Gamel	
DM		us-	5,289,021	02-22-1994	El Gamel	
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		us-				
		us-				
		us-				

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

Examiner Signature	/David Mis/ (09/27/2006)	Date Considered	09/27/2006
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SEP 07 2004

Substitute for Form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 2 of 3 Attorney Docket Number 004363.P005C

Complete if Known

Application Number	10/810,444
Filing Date	March 26, 2004
First Named Inventor:	David M. Colleran
Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
DM		HERSHENSON, M., et al., "GPCAD: A Tool for CMOS Op-Amp Synthesis" 8 pages, Proceedings of the IEEE/ACM International Conference on Computer Aided Design (ICCAD), pp. 296-303, November 1998.	
DM		HERSHENSON, M., et al., "Posynomial models for MOSFETs" 9 pages, July 7, 1998.	
DM		CHANG, H, et al., "A Top-Down, Constraint-Driven Design Methodology for Analog Integrated Circuits" 6 pages, IEEE 1992 Custom Integrated Circuits Conference.	
DM		CHAVEZ, J., et al, "Analog Design Optimization: A Case Study" 3 pages, IEEE, January 1993.	
DM		GEILEN, G., et al., "Analog Circuit Design Optimization Based on Symbolic Simulation and Simulated Annealing", pp. 707-713, IEEE Journal of Solid-State Circuits, Vol. 25, No. 3, June 1990.	
DM		FISHBURN, J, et al., "TILOS: A Posynomial Programming Approach to Transistor Sizing" pp. 326-328, IEEE, 1985.	
DM		MAULIK, P., et al., "Integer Programming Based on Topology Selection of Cell-Level Analog Circuits", 12 pages, IEEE Transactions On Computer-Aided Design Of Integrated Circuits And Systems, Vol. 14, No. 4, April 1995.	
DM		SWINGS, K., et al., "An Intelligent Analog IC Design System Based On Manipulation Of Design Equations" pp. 8.6.1- 8.6.4, IEEE 1990, Custom Integrated Circuits Conference.	
DM		NESTEROV, Y., et al., "Interior-Point Polynomial algorithms in Convex Programming" 8 pgs., 1994, Society for Industrial and Applied mathematics.	
DM		YANG, H.Z., et al., "Simulated Annealing Algorithm with Multi-Molecule: an Approach to Analog Synthesis" pp. 571-575, IEEE, 1996.	
DM		WONG, D.F., et al., "Simulated Annealing For VLSI Design" 6 pages, 1998, Kulwer Academic Publishers.	

Examiner Signature	/David Mis/ (09/27/2006)	Date Considered	09/27/2006
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¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English Translation is attached.
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SEP 07 2004

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Complete if Known

Application Number	10/810,444
Filing Date	March 26, 2004
First Named Inventor:	David M. Colleran
Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned

Sheet 3 of 3 Attorney Docket Number 004363.P005C

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
DM		MAULIK, P., et al., "Sizing of Cell-Level Analog Circuits Using Constrained Optimization Techniques" pp. 233-241, IEEE Journal of Solid-State Circuits, Vol. 28, No. 3, March 1993.	
DM		OCHOTTA, E., et al., "Synthesis of High -Performance Analog Circuits in ASTRX/OBLS" pp. 273-295, IEEE Transactions on Computer-Aided Design of Integrated Circuits And Systems, Vol. 15, No. 3, March 1996.	
DM		WRIGHT, S., "Primal-Dual Interior-Point Methods" pp. 1-3, http://www.siam.org/books/wright , Printed August 19, 1998.	
DM		SHYU, J., et al., "Optimization-Based Transistor Sizing" pp. 400-408, IEEE Journal of Solid-State Circuits, Vol. 23, No. 2, April 1998.	
DM		WRIGHT, S., "Primal-Dual Interior-Point Methods" 14 pages, 1997, Society for Industrial and Applied Mathematics.	
DM		VAN LAARHOVEN, P.J.M., et al., "Simulated Annealing: Theory and Applications" 26 pages, 1987, Kluwer Academic Publishers.	
DM		HERSHENSON, M., et al., "CMOS Operational Amplifier Design and Optimization via Geometric Programming" pp. 1-4, Analog Integrated Circuits, Stanford University.	
DM		AGUIRRE, M.A., et al., "Analog Design Optimization by means of a Tabu Search Approach" pp. 375-378.	
DM		MEDEIRO, F., et al., "A Statistical Optimization-Based Approach for Automated Sizing of Analog Cells", pp. 594-597, Dept. of Analog Circuit Design.	
DM		SPATNEKAR, S., "Wire Sizing as a Convex Optimization Problem: Exploring the Area-Delay Tradeoff" 27 pages, Dept. of Electrical and Computer Engineering.	
DM		SU, H., et al., "Statistical Constrained Optimization of Analog MOS Circuits Using Empirical Performance Models" pp. 133-136.	
DM		VASSILIOU, I., et al., "A Video Driver System Designed Using a Top-Down, Constraint-Driven Methodology" 6 pages.	
DM		SAPATNEKAR, S, et al., "An Exact Solution to the Transistor Sizing Problem for CMOS Circuits Using Convex Optimization" 35 pages.	

Examiner Signature	/David Mis/ (09/27/2006)	Date Considered	09/27/2006
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1PW

TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

		Application No.	10/810,444
		Filing Date	March 26, 2004
		First Named Inventor	David M. Colleran
		Art Unit	2123
		Examiner Name	Unassigned
Total Number of Pages in This Submission	7	Attorney Docket Number	4363P005C

ENCLOSURES (check all that apply)

<input checked="" type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC	
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences	
<input type="checkbox"/> Amendment / Response	<input type="checkbox"/> Petition	<input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)	
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition to Convert a Provisional Application	<input type="checkbox"/> Proprietary Information	
<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	<input type="checkbox"/> Status Letter	
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Terminal Disclaimer	<input checked="" type="checkbox"/> Other Enclosure(s) (please identify below):	
<input type="checkbox"/> Express Abandonment Request	<input type="checkbox"/> Request for Refund	Return Receipt Postcard.	
<input checked="" type="checkbox"/> Information Disclosure Statement	<input type="checkbox"/> CD, Number of CD(s)		
<input checked="" type="checkbox"/> PTO/SB/08	<input type="checkbox"/> Landscape Table on CD		
<input type="checkbox"/> Certified Copy of Priority Document(s)	Remarks		
<input type="checkbox"/> Response to Missing Parts/ Incomplete Application			
<input type="checkbox"/> Basic Filing Fee			
<input type="checkbox"/> Declaration/POA			
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53			

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Robert B. O'Rourke, Reg. No. 46,972 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
Signature	
Date	2/27/06

CERTIFICATE OF MAILING/TRANSMISSION

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Typed or printed name	Carla Vignola
Signature	
Date	2/27/06

MAR 02 2006

TRADEMAKES EE TRANSMITTAL for FY 2005		<i>Complete if Known</i>	
Patent fees are subject to annual revision.		Application Number	10/810,444
<input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27.		Filing Date	March 26, 2004
TOTAL AMOUNT OF PAYMENT		(<u>\$</u>)	0.00
		First Named Inventor	David M. Colleran
		Examiner Name	Unassigned
		Art Unit	2123
		Attorney Docket No.	4363P005C

METHOD OF PAYMENT (*check all that apply*)

- Check Credit card Money Order None Other (please identify): _____
 Deposit Account Deposit Account Number: 02-2666 Deposit Account Name: Blakely, Sokoloff, Taylor & Zafman LLP

For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)

- Charge fee(s) indicated below Charge fee(s) indicated below, except for the filing fee
 Charge any additional fee(s) or underpayment of fee(s)
under 37 CFR §§ 1.16, 1.17, 1.18 and 1.20. Credit any overpayments

FEE CALCULATION

1. EXTRA CLAIM FEES

Total Claims		Extra Claims	Fee from below	Fee Paid
Independent Claims	8	20° = 0	50.00	\$0.00
Multiple Dependent	2	3° = 0	200.00	\$0.00

Large Entity	Small Entity		
Fee	Fee	Fee Description	
Code	Code	(9)	
1202	50	2202	25 Claims in excess of 20
1201	200	2201	100 Independent claims in excess of 3
1203	350	2203	150 Multiple Dependent claim, if not paid
1204	750	2204	395 **Reissue Independent claims over original patent
1205	300	2205	150 **Reissue claims in excess of 20 and over original patent

**or number previously paid. If greater, For Reissues, see below.*

2. ADDITIONAL FEES

Large Entity		Small Entity		Fee Description
Fee Code	Fee (\$)	Fee Code	Fee (\$)	
1051	130	2051	65	Surcharge - late filing fee or oath
1052	50	2052	25	Surcharge - late provisional filing fee or cover sheet
2053	130	2053	130	Non-English specification
1251	120	2251	60	Extension for reply within first month
1252	450	2252	225	Extension for reply within second month
1253	1,020	2253	510	Extension for reply within third month
1254	1,590	2254	785	Extension for reply within fourth month
1255	2,160	2255	1,080	Extension for reply within fifth month
1401	500	2401	250	Notice of Appeal
1402	500	2402	250	Filing a brief in support of an appeal
1403	1,000	2403	500	Request for oral hearing
1451	1,510	2451	1,510	Petition to Institute a public use proceeding
1460	130	2460	130	Petitions to the Commissioner
1807	50	1807	50	Processing fee under 37 CFR 1.17(q)
1808	180	1808	180	Submission of Information Disclosure Stmt
1809	790	1809	395	Filing a submission after final rejection (37 CFR § 1.17(r))
1810	790	2810	395	For each additional invention to be examined (37 CFR § 1.17(r))
Other fee (specify)				
Submission of IDS before first Office Action				

Fee Paid

(S) 0.00

SUBMITTED BY

Name (First/Type)	Robert B. O'Rourke	Registration No. (Attorney/Agent)	46,972	Telephone	(408) 720-8300
Signature				Date	2/27/06

Based on PTO/SB/17 (12-04) as modified by PTO-SB-01-14, 7-1-04.

Based on PTO/SB/17 (12-04) as modified by Blakely, Sokoloff, Taylor & Zafman (w/r) 12/15/2004.
SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450



Docket No.: 4363P005C

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

DAVID M. COLLERAN, ET AL.

Application No.: 10/810,444

Filed: March 26, 2004

For: **Automatic Phase Lock Loop Design Using
Geometric Programming**

Art Group: 2123

Examiner: Unassigned

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

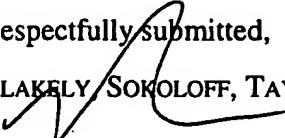
The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Date: 2/27/06

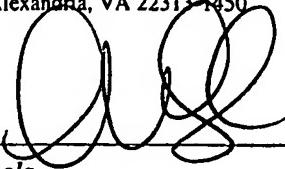
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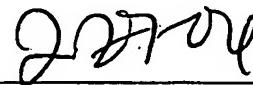
BLAKELY SOKOLOFF, TAYLOR & ZAFMAN LLP


Robert B. O'Rourke, Reg. No. 46,972

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Telephone: (408) 720-8300

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Carla Vignola


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Examiner Signature	/David Mis/ (09/27/2006)	Date Considered	09/27/2006
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¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English Translation is attached.
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IDS - 09/18/2006

70

TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

		Application No.	10/810,444
		Filing Date	March 26, 2004
		First Named Inventor	David M. Coleran
		Art Unit	2123
		Examiner Name	Unassigned
Total Number of Pages in This Submission	7	Attorney Docket Number	4363P005C

ENCLOSURES (check all that apply)

<input checked="" type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> After Allowance Communication to TC <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): Return Receipt Postcard.
<input type="checkbox"/> Amendment / Response <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Terminal Disclaimer	
<input type="checkbox"/> Express Abandonment Request	<input type="checkbox"/> Request for Refund	
<input checked="" type="checkbox"/> Information Disclosure Statement <input checked="" type="checkbox"/> PTO/SB/08	<input type="checkbox"/> CD, Number of CD(s) <input type="checkbox"/> Landscape Table on CD	
<input type="checkbox"/> Certified Copy of Priority Document(s)		
<input type="checkbox"/> Response to Missing Parts/ Incomplete Application <input type="checkbox"/> Basic Filing Fee <input type="checkbox"/> Declaration/POA		
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		
Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Robert B. O'Rourke, Reg. No. 46,972 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
Signature	
Date	

CERTIFICATE OF MAILING/TRANSMISSION

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Signature		Date	9-12-2006

Based on PTO/SB/21 (09-04) as modified by Blakely, Sokoloff, Taylor & Zafman (wtr) 11/30/2005.
SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450



FEE TRANSMITTAL for FY 2005

Patent fees are subject to annual revision.

Applicant claims small entity status. See 37 CFR 1.27.

TOTAL AMOUNT OF PAYMENT (\$)
0.00

Complete if Known	
Application Number	10/810,444
Filing Date	March 26, 2004
First Named Inventor	David M. Colleran
Examiner Name	Unassigned
Art Unit	2123
Attorney Docket No.	4363P005C

METHOD OF PAYMENT (check all that apply)

Check Credit card Money Order None Other (please identify): _____

Deposit Account Deposit Account Number: 02-2666 Deposit Account Name: Blakely, Sokoloff, Taylor & Zafman LLP

For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)

Charge fee(s) indicated below Charge fee(s) indicated below, except for the filing fee
 Charge any additional fee(s) or underpayment of fee(s) Credit any overpayments
under 37 CFR §§ 1.16, 1.17, 1.18 and 1.20.

FEE CALCULATION

Large Entity Small Entity

Fee Code	Fee (\$)	Fee Code	Fee (\$)	Fee Description	Fee Paid
1051	130	2051	65	Surcharge - late filing fee or oath	
1052	50	2052	25	Surcharge - late provisional filing fee or cover sheet.	
2053	130	2053	130	Non-English specification	
1251	120	2251	60	Extension for reply within first month	
1252	450	2252	225	Extension for reply within second month	
1253	1,020	2253	510	Extension for reply within third month	
1254	1,590	2254	795	Extension for reply within fourth month	
1255	2,160	2255	1,080	Extension for reply within fifth month	
1401	500	2401	250	Notice of Appeal	
1402	500	2402	250	Filing a brief in support of an appeal	
1403	1,000	2403	500	Request for oral hearing	
1451	1,510	2451	1,510	Petition to institute a public use proceeding	
1460	130	2460	130	Petitions to the Commissioner	
1807	50	1807	50	Processing fee under 37 CFR 1.17(q)	
1806	180	1806	180	Submission of Information Disclosure Stmt	
1809	790	1809	395	Filing a submission after final rejection (37 CFR § 1.129(a))	
1810	790	2810	395	For each additional invention to be examined (37 CFR § 1.129(b))	

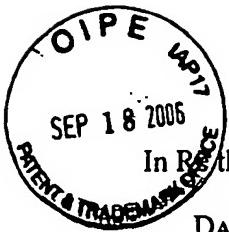
Other fee (specify) Submission of IDS prior to first Office Action 0.00

SUBTOTAL (2) (\$) 0.00

SUBMITTED BY

Complete (if applicable)

Name (Print/Type)	<u>Robert B. O'Rourke</u>	Registration No. (Attorney/Agent)	46,972	Telephone	(408) 720-8300
Signature				Date	<u>9/12/06</u>



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

DAVID M. COLLERAN, ET AL.

Application No.: 10/810,444

Filed: March 26, 2004

For: Automatic Phase Lock Loop Design Using
Geometric Programming

Art Group: 2123

Examiner: Unassigned

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

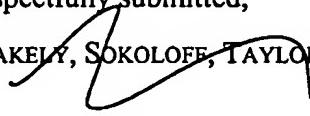
In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Date: 9/12/06

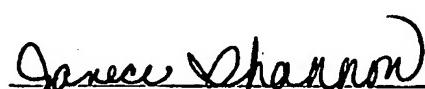
Respectfully submitted,


BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP


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Jayece Shannon 9-12-2006
Jayece Shannon Date



<p>Substitute for form 1449A/PTO</p> <h1 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h1> <p style="text-align: center;"><i>(use as many sheets as necessary)</i></p>				Complete if Known	
Sheet	1	of	1	Application Number	10/810,444
				Filing Date	March 26, 2004
				First Named Inventor	David M. Colleran
				Art Unit	2123
				Examiner Name	Unassigned
				Attorney Docket Number	4363P005C

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature	/David Mis/ (09/27/2006)	Date Considered	09/27/2006
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**Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.*

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 801.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 18 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 08/11/2003.

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